
Untouchable Mulk
Raj Anand Ebook
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Description of the Related Art As the semiconductor industry has developed, the integration of various metal-oxide semiconductors (MOS) has been dramatically increased. For instance, complementary MOS logic circuits are widely used in various integrated circuits. However, such complementary MOS logic circuits may be destroyed by a surge voltage due to a change of power voltage on a power line, etc. In order to reduce the destructive effect of such a surge voltage, an NMOS transistor with a gate formed of an n-type metal-oxide semiconductor (NMOS) and a PMOS transistor with a gate formed of a p-type metal-oxide semiconductor (PMOS) have

been used. However, as a size of the devices becomes smaller, the destruction from the surge voltage may become more serious. In order to prevent the destruction by the surge voltage, a conventional semiconductor device includes a grounded transistor, i.e., a source metal-oxide semiconductor (MOS) transistor and a drain MOS transistor, and a lateral double-diffused MOS (LDMOS) transistor including a drain extension region and a source extension region. However, as a size of the semiconductor device is scaled down to the deep submicron technology, the conventional structure may not sufficiently reduce the destruction by the surge voltage, and thus, the parasitic bipolar device may be turned on due to the turn-on breakdown voltage. FIG. 1 is a cross-sectional view of a conventional semiconductor device including a drain extension region and a gate metal-oxide semiconductor (MOS) transistor. FIG. 2 is a cross-sectional view of a conventional semiconductor device including a source extension region and a gate MOS transistor. The conventional semiconductor devices are described in

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 $x^2 + 0x + 1$ $x^3 + 1 - 2/3x$ $x^4 - 5$. Determine
 $p(2)$. Let $f = -2 - -4$. Let $p(0)$
 $= -0x^3 + 4x^2 + 6x + 2$. Give $p(5)$
 $)$.